Docket No.: 1016-012

the specification of which

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter claimed and for which a patent is sought on the invention entitled:

VIA-SEA LAYOUT IN INTEGRATED CIRCUITS

_	was filed on	as Application	Serial No and was amende	ed on (i	if applicable).	
	I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I do not know and do not believe the same was ever known or used in the United States of America before this invention thereof or more than one year prior to this application, and that the same was not in public use or on sale in the United States of America more than one year prior to this application. I acknowledge the duty to disclose information which is known to me to be material to patentability in accordance with title 37, Code of Federal Regulations section 1.56.					
Am the pric me						
I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or Section 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT international application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:						
Pric	or Foreign Appl	lication(s):				
				Priority	Claimed	
	NI	O	TS /3.6 /1.57 C1 1	•		
	Number	Country	Day/Month/Year filed	Yes	No No	
		enefit under 35 U	Day/Month/Year filed USC 119(e) of any United Sta	Yes	<u>No</u>	
app	reby claim the be	enefit under 35 t		Yes	<u>No</u>	
appi Pri c	reby claim the bolication(s) listed	enefit under 35 to below: Application(s):		Yes	<u>No</u>	

the national or PCT international filing date of this application:

Regulations, Section 1.56 which occurred between the filing date of the prior application and

Docket No.: 1016-012

Prior U.S. Application(s):

Serial No.

Filing Date

Status: Patented, Pending, Abandoned

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby revoke any previous Powers of Attorney and appoint the following attorney(s) and/or agent(s), each said individual being a member or associate of The Law Offices of Mikio Ishimaru or being employed by Chartered Semiconductor Manufacturing Ltd.:

Mikio Ishimaru

Reg. No. 27,449

for so long as they remain with such law offices or company with full power of substitution and revocation, to prosecute this application and to transact in connection therewith all business in the Patent and Trademark Office and before competent International Authorities; said appointment to be to the exclusion of myself and my other attorney(s); and all future correspondence should be addressed to:

> Mikio Ishimaru The Law Offices of Mikio Ishimaru 1110 Sunnyvale – Saratoga Rd., Suite A1 Sunnyvale, California 94087

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Inventor's signature: Date: Chivukula Subramanyam Full name of joint inventor: Citizenship: India Residence Address, including Blk 557, Aug Mo Kio Ave 10 country: #07-1866 Singapore 560557 Post Office Address: c/o Chartered Semiconductor Manufacturing Ltd. 60 Woodlands Industrial Park D. Street 2 Singapore 738406 Date: 20/12/2001 Inventor's signature: Full name of joint inventor: Thow Phock Chua Citizenship: **Singapore** Residence Address, including Blk 42 Cassia Crescent country: #05-210 Singapore, 390042 Post Office Address: c/o Chartered Semiconductor Manufacturing Ltd. 60 Woodlands Industrial Park D. Street 2

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